due to a failure in the other of said two buses.

26. (Amended) A storage system according to claim 24, wherein said memory can be referred to by an external processor.
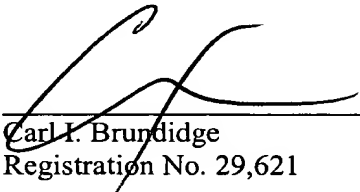
REMARKS

Attached hereto is a marked-up version of the changes made to the claims by the current Amendment. The attached page is captioned "**Version with markings to show changes made**".

Entry of the above amendments prior to examination is respectfully requested.

Please charge any shortage in fees due in connection with the filing of this paper, or credit any overpayment of fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (500.33021CX5).

Respectfully submitted,

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IN THE CLAIMS

Please cancel claims 22 and 25 without prejudice or disclaimer of the matter therein.

Please amend the claims as follows:

21.   (Amended)   A storage system comprising:

a plurality of host adaptors connected to at least one host device, which form interfaces for the host device;

a plurality storage devices for storing therein data transferred from the host device;

a plurality of disk adaptors connected to said storage devices, which form interfaces for said storage devices;

a cache for temporarily storing therein data transferred between said plurality of host adaptors and said plurality of disk adaptors; ~~and~~

two buses, connected to said plurality of host adaptors, said plurality of disk adaptors, and said cache, which transfer data among said plurality of host adaptors, said plurality of disk adaptors, and said cache, wherein said two buses operate as a pair of buses having a transfer ability larger than one of said two buses; and

<u>a memory for storing a status of which of said buses is available for use due to a failure in the other of said buses.</u>

24.   (Amended)   A storage system comprising:

a plurality of host adaptors connected to at least one host device, which form interfaces for the host device;

a plurality of storage devices for storing therein data transferred from the host device;

a plurality of disk adaptors connected to said storage devices, which form interfaces for said storage devices;

a plurality of caches for temporarily storing therein data transferred between said plurality of host adaptors and said plurality of disk adaptors; ~~and~~

two buses, connected to said plurality of host adaptors, said plurality of disk adaptors, and said plurality of caches, which transfer data among said plurality of host adaptors, said plurality of disk adaptors, and said plurality of caches, wherein said two buses operate as a pair of buses having a transfer ability larger than one of said two buses; and

<u>a memory for storing a status of which of said buses is available for use due to a failure in the other of said buses.</u>

26.	(Amended)	A storage system according to claim ~~25~~<u>24</u>, wherein said memory can be referred to by an external processor.